Cheuk Y. Yu

U.S. CITIZEN **EMAIL:** *cheukyu93@gmail.com* **LINKEDIN:** *www.linkedin.com/in/cheukyu*

HIGHLIGHTS:

- 5+ years of equivalent experience in PCB design using **Altium Designer**
- 4+ years of equivalent experience in post-silicon IC validation and production testing

SKILLS:

- Programming Languages: Visual Studio C++, LabView
- Tools: Altium Designer (PCB), TestStand, Spotfire, Jira

WORK EXPERIENCE:

Validation and Characterization Engineer at Texas Instruments March 2019 ~ Present

Precision Amplifiers Group

- Performing Extended Validation on precision Operational Amplifiers, such as Abs Max Ratings
- Validating Startup conditions of precision Operational Amplifiers over temperature
- Performing data analysis and presentation using **Spotfire**
- Developing new hardware (PCBs) and software (using **LabView/TestStand**) to streamline validation process

Product Engineer Rotation Program at Texas Instruments

2nd Rotation: Test Engineer, Precision Amplifiers Group March 2018 ~ February 2019

- Developed final test (production level) **C++** programs to test precision Operational Amplifiers on **ETS-364** and **ETS-88** platforms, including setting test limits to ensure acceptable levels of yield (typically >95%)
- Communicated with overseas production sites to ensure smooth production of new product release
- Assisted a senior test engineer on developing new hardware for a faster (lowered test cost) and more accurate test solution of precision Operational Amplifiers

1st Rotation: Validation Engineer, Precision Amplifiers Group February 2017 ~ February 2018

- Validated and characterized DC parameters of precision Operational Amplifiers over temperature
- Validated Startup conditions of precision Operational Amplifiers over temperature
- Performed data analysis and presentation using **Spotfire**
- Created datasheet curves for pre-release precision Operational Amplifiers
- Designed PCB Layout in **Altium Designer** upon request

Validation Engineering Intern at Texas Instruments June 2016 ~ September 2016

- Designed a system that measures the long-term voltage offset drift of operational amplifiers, which can run at most 8 different sets of products with 28 samples each, under a constant temperature and power supply level
  - Selected the appropriate hardware for the system, such as oil bath and uninterruptible power supplies
  - Designed the power regulating system that is customizable for different products
  - Designed and assembled the PCBs, as well as validated the functionality using bench equipment

Student Researcher of ELFIN (UCLA CubeSat Project) May 2014 ~ June 2016

- Worked on a nanosatellite mission that was launched in 2018 to study space weather
- Built schematic diagrams and PCB layouts for avionics and ground support equipment using **Altium Designer**
- Wrote up documents to guide technicians on populating most PCBs in the mission
- Designed an Arduino-based programmable constant current load board with 15 channels
- Tested and debugged circuit boards like the linear power supply boards (LIPS), using datasheets and bench equipment
- Created components' footprint and organized the component libraries in **Altium Designer**

EDUCATION:

University of California, Los Angeles (UCLA) September 2012 ~ December 2016

B.S., *magna cum laude,* Electrical Engineering **Overall GPA: 3.83/4.00** **Major GPA: 3.90/4.00**